Amendments to the Specification

Rewrite the paragraph at page 6, lines 28 to page 7, line 2 as follows:

FIGURE 5 illustrates a signal diagram of outputs 22,24,26. The mode word output 22 toggles to indicate the border between words (a sequence of a predetermined number of bits). The mode bit output 24 toggles to indicates the location of bits in the mode data 26. The mode data 26 includes address bits 64 that indicate which mode address is being modified by the word. The mode data 26 also includes function bits 66 and mute bit 68. The mute bit 68 is a signal that, when properly indicated by mode word and mode bit outputs 22,24, disables the analog output by activating the DAC mute. The DAC mute can operate through a digital filter in the DAC which processes the digital data before it is converted into analog data. A pull-down circuit in the DAC coupled to mode data input 27 creates a low voltage at the input if the disable signal is absent.